

GODAVARI INSTITUTE OF ENGINEERING & TECHNOLOGY

NH-16, CHAITANYA KNOWLEDGE CITY, RAJAHMUNDRY-533296

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING**SPECIALIZATION: DE & CS****COURSE STRUCTURE-R14****I SEMESTER**

S.No	SUBJECT CODE	SUBJECT	Periods per week			C	Scheme of Examination Max.Marks		
			T	P	D		Int.	Ext.	Total
1	14241101	Digital System Design	4	-	-	3	40	60	100
2	14241102	Detection& Estimation Theory	4	-	-	3	40	60	100
3	14241103	Digital Data Communication	4	-	-	3	40	60	100
4	14241104	Advanced Digital Signal processing	4	-	-	3	40	60	100
5	14241161(A) 14241161(B)	Elective I 1. VLSI Technology & Design 2. Transformation Techniques	4	-	-	3	40	60	100
6	14241162(A) 14241162(B)	Elective II 1. Optical communication Technology 2. Statistical Signal Processing	4	-	-	3	40	60	100
7	14241111	Laboratory 1.Design and Simulation lab	-	3	-	2	40	60	100
		TOTAL	24	3	-	20	280	420	700

II-SEMESTER

S.No	SUBJECT CODE	SUBJECT	Periods per week			C	Scheme of Examination Max.Marks		
			T	P	D		Int.	Ext.	Total
1	14241201	Coding Theory & Applications	4	-	-	3	40	60	100
2	14241202	Embedded Real Time Operating Systems	4	-	-	3	40	60	100
3	14241203	Image and Video Processing	4	-	-	3	40	60	100
4	14241204	Wireless Communication and Networks	4	-	-	3	40	60	100
5	14241263(A) 14241263(B)	Elective III 1.CMOS Analog and Digital IC Design 2.Advanced Computer Architecture	4	-	-	3	40	60	100
6	14241264(A) 14241264(B)	Elective IV 1.DSP processor and Architecture 2.EMI/EMC	4	-	-	3	40	60	100
7	14241211	Laboratory Advanced Communication Laboratory	4	3	-	2	40	60	100
		TOTAL	24	3	-	20	280	420	700

T-THEORY P-PRACTICAL D-DRAWING C-CREDITS Int.-INTERNAL Ext.-EXTERNAL

III SEMESTER

S.No	SUBJECT CODE	SUBJECT	Periods per week			C	Scheme of Examination Max.Marks		
			T	P	D		Int.	Ext.	Total
1	14241381	Seminar -I	1	-	-	2	50	-	50
2	14241341	Project work -I	-	-	-	18	-	-	-
		TOTAL	1	-	-	20	50	-	50

T-THEORY P-PRACTICAL D-DRAWING C-CREDITS Int.-INTERNAL Ext.-EXTERNAL

IV SEMESTER

S.No	SUBJECT CODE	SUBJECT	Periods per week			C	Scheme of Examination Max.Marks		
			T	P	D		Int.	Ext.	Total
1	14241481	Seminar -II	1	-	-	2	50	-	50
2	14241441	Project work -II	-	-	-	18	-	-	-
		TOTAL	1	-	-	20	50	-	50

T-THEORY P-PRACTICAL D-DRAWING C-CREDITS Int.-INTERNAL Ext.-EXTERNAL

Elective I

14241161(A) - VLSI Technology & Design

14241161(B) - Transformation Techniques

Elective II

14241162(A) - Optical communication Technology

14241162(B) - Statistical Signal Processing

Elective III

14241263(A) - CMOS Analog and Digital IC Design

14241263(B) - Advanced Computer Architecture

Elective IV

14241264(A) - DSP processor and Architecture

14241264(B) - EMI/EMC